



SKSVMA Charitable Trust (Regd.)

Smt. Kamala & Sri Venkappa M Agadi College of Engineering & Technology

Lakshmeshwar 582116 Dist: Gadag

Department of Electronics and Communication Engineering

5<sup>th</sup> SEMESTER TIME TABLE

Class Room: 5002 (Ground Floor)

W E F: 29-7-2019

Class Teacher: Prof. Sanjay V. Chowdhary

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30
Mon	MED	VHDL	T E A	OS	OOPC /8051	L U N C H	DSP LAB (B1) / HDL LAB (B2)		
Tue	OS	MED		VHDL	DSP		DSP LAB (B2) / HDL LAB (B3)		
Wed	VHDL	DSP		MED	ITC		DSP LAB (B3) / HDL LAB (B1)		
Thu	OOPC /8051	ITC	B R E A K	VHDL	MED	B R E A K			
Fri	DSP	OS		ITC	OOPC /8051				
Sat	ITC	DSP		Association Activity					

**Subject Code**

17ES51  
17EC52  
17EC53  
17EC54  
17EC553  
17EC562  
17EC563  
17ECL57  
17ECL58

**Subject**

Management & Entrepreneurship Development  
Digital Signal Processing  
Verilog HDL  
Information Theory & coding  
Operating System  
OOP using C++  
8051 Microcontroller  
DSP Lab  
HDL Lab

MED  
DSP  
VHDL  
ITC  
OS  
C++  
8051  
DSP Lab  
HDL Lab

**Faculty**

Prof. Basavaraj Soratur.  
Prof. Maltesh I. G.  
Prof. Sanjay V. Chowdhary.  
Prof. Shashidhar P. K.  
Prof. Sharan Kumar.  
Prof. Sunil B.  
Prof. Kiran K. Kotin.  
Prof. Maltesh I. G.  
Prof. Sanjay V. Chowdhary.

Coordinator

20/07/2019  
Head of Department  
HOD  
Electronics & Communication  
Smt. Kamala & Shri Venkappa M. Agadi  
College of Engineering & Technology

PRINCIPAL  
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