

4th SEMESTER TIME TABLE

Class Room: 4205 (Second Floor)

W. E. F.: 17-02-2020

Class Teacher: Mr. Krishna Deshpande

	09.30am TO 10.30am	10.30am TO 11.30am	11.30am TO 11.45am	11.45am TO 12.45pm	12.45pm TO 01.45pm	01.45pm TO 02.30pm	02.30pm TO 03.30pm	03.30pm TO 04.30pm	04.30pm TO 05.30pm
Mon	ESLA	AC	T E A C H E R E A K	SS	MAT-IV	L U N C H B R E A K	MC LAB (B1) / AC LAB (B2)		
Tue	AC	SS		MAT-IV (T)			MC LAB (B2) / AC LAB (B3)		
Wed	ESLA	MAT-IV		AC	CS		MC LAB (B3) / AC LAB (B1)		
Thu	MAT-IV	MC		CS	SS		KAK	SS (T)	
Fri	CS (T)			MC	ESLA		MATDIP-II		
Sat	AC (T)			CS	MC				

Subject Code**Subject**

18MAT41	Complex Analysis, Probability & Statistical Methods	Maths-IV
18EC42	Analog Circuits	AC
18EC43	Control Systems	CS
18EC44	Engineering Statistics & Linear Algebra	ESLA
18EC45	Signals & Systems	SS
18EC46	Microcontroller	MC
18ECL47	Microcontroller Laboratory	MC Lab
18ECL48	Analog Circuits Laboratory	AC Lab
18KAK49	Aadalitha Kannada	KAK
18MATDIP41	Additional Mathematics – II	MATDIP-II

Faculty

Mr. Girish Y.
 Dr. Subhas Meti.
 Mr. Sunil Begumpur
 Mr. Basavaraj S. Soratur
 Mr. Krishna G. Deshpande
 Mr. Kiran K. Kotin
 Dr. Sharan Kumar & Mr. Kiran K. Kotin
 Dr. Subhas Meti & Mr. Krishna G. D.
 Adv. B. S. Baleshwarmath
 Mathematics Department

Coordinator**HOD****PRINCIPAL**



6th SEMESTER TIME TABLE

Class Room: 4208 (2nd Floor)

W. E. F.: 17-02-2020

Class Teacher: Mr. Sanjay V. Chowdhary

	09.30am TO 10.30am	10.30am TO 11.30am	11.30am TO 11.45am	11.45am TO 12.45pm	12.45pm TO 01.45pm	01.45pm TO 02.30pm	02.30pm TO 03.30pm	03.30pm TO 04.30pm	04.30pm TO 05.30pm
Mon	VLSI	CCN	T E A B R E A K	ARMES	C++/DSDV	L U N C H B R E A K	EC LAB (B1) / CN LAB (B2)		
Tue	ME/DSS	VLSI		ARMES	DC		EC LAB (B2) / CN LAB (B3)		
Wed	CCN	C++/ DSDV		DC	ARMES		EC LAB (B3) / CN LAB (B1)		
Thu	C++/ DSDV	CCN		ME/DSS	VLSI		Association Activity		
Fri	ARMES	ME/DSS		DC	CCN		ES	Association Activity	
Sat	VLSI	DC		Association Activity					

Subject Code

Subject

17EC61
17EC62
17EC63
17EC64
17EC654
17EC655
17EC661
17EC663
17ECL67
17ECL68
17CIV18

Digital Communication
ARM Microcontroller & Embedded Systems
VLSI Design
Computer Communication Networks
Digital Switching System
Microelectronics
Data structure using C++
Digital System Design using Verilog
Embedded Controller Lab
Computer Networks Lab
Environmental Studies

DC
ARMES
VLSI
CCN
DSS
ME
C++
DSDV
EC Lab
CN Lab
ES

Faculty

Mr. Shashidhar P. K.
Mr. Basavaraj S. Soratur
Dr. Sharan kumar
Mr. Manjunathraddi Bentur
Mr. S V. Mahapurush
Mr. Sanjay V. Chowdhary
Mr. Sunil Begumpur
Mr. Santosh S. Bujari
Mr. Maltesh I. G. & Mr. Basavaraj S. Soratur
Mr. Shashidhar P. K. & Mr. Manjunathraddi Bentur
Mr. Laxman Patil (Civil dept)

Coordinator

HOD

PRINCIPAL



8th SEMESTER TIME TABL

Class Room: 5002 (Ground Floor)

W. E. F.: 17-02-2020

Class Teacher: Mr. Shashidhar P K

	09.30am TO 10.30am	10.30am TO 11.30am	11.30am TO 11.45am	11.45am TO 12.45pm	12.45pm TO 01.45pm	01.45pm TO 02.30pm	02.30pm TO 03.30pm	03.30pm TO 04.30pm	04.30pm TO 05.30pm
Mon	WCLTE	FON	T E A B R E A K	NCS		L U N C H B R E A K	PROJECT WORK		
Tue	FON	NCS		WCLTE			SEMINAR		
Wed	NCS	WCLTE		FON			PROJECT WORK		
Thu	FON	WCLTE		IPP			SEMINAR		
Fri	PROJECT WORK			IPP			Association Activity		
Sat	Association Activity			Association Activity					

Subject Code

Subject

Faculty

15EC81	Wireless Cellular and LTE 4G Broadband	WCLTE	Mr. S V. Mahapurush
15EC82	Fiber Optics & Networks	FON	Mr. Maltesh I. G.
15EC835	Network & Cyber Security	NCS	Mr. Shashidhar P. K.
15EC84	Internship/Professional Practice	IPP	Mr. Sunil B. & Mr. Krishna G. D.
15ECP85	Project Work	Project Work	Mr. Sanjay V. Chowdhary
15ECS86	Seminar	Seminar	Mr. Santosh S. Bujari

Coordinator

HOD

PRINCIPAL



MASTER TIME TABLE
Even SEMESTER (2019-20)

	Semester	09.30am TO 10.30am	10.30am TO 11.30am	11.30am TO 11.45am	11.45am TO 12.45pm	12.45pm TO 01.45pm	01.45pm TO 02.30pm	02.30pm TO 03.30pm	03.30pm TO 04.30pm	04.30pm TO 05.30pm						
Mon	IV	ESLA	AC	T E A	SS	MAT-IV	L U N C H	MC LAB (B1) / AC LAB (B2)								
	VI	VLSI	CCN		ARMES	C++/DSDV		EC LAB (B1) / CN LAB (B2)								
	VIII	WCLTE	FON		NCS			PROJECT WORK								
Tue	IV	AC	SS		B R E A K	MAT-IV (T)		B R E A K	MC LAB (B2) / AC LAB (B3)							
	VI	ME/DSS	VLSI			ARMES			DC	EC LAB (B2) / CN LAB (B3)						
	VIII	FON	NCS			WCLTE				SEMINAR						
Wed	IV	ESLA	MAT-IV			B R E A K			AC	CS	B R E A K	MC LAB (B3) / AC LAB (B1)				
	VI	CCN	C++/ DSDV						DC	ARMES		EC LAB (B3) / CN LAB (B1)				
	VIII	NCS	WCLTE						FON			PROJECT WORK				
Thu	IV	MAT-IV	MC						B R E A K	CS		SS	B R E A K	KAK		
	VI	C++/ DSDV	CCN							ME/DSS		VLSI		Association Activity		
	VIII	FON	WCLTE							IPP		SEMINAR				
Fri	IV	CS (T)		B R E A K			MC			ESLA		B R E A K		MATDIP-II		
	VI	ARMES	ME/DSS				DC			CCN				ES	Association Activity	
	VIII	PROJECT WORK					IPP			Association Activity						
Sat	IV	AC (T)			B R E A K		CS	MC		B R E A K						
	VI	VLSI	DC				Association Activity									
	VIII	Association Activity					Association Activity									

Coordinator

HOD

PRINCIPAL