

**Class Room: 4205 (Second Floor)**

**W E F: 01-09-2020**

**Class Teacher: Prof. Sunil B.**

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30	
<b>Mon</b>	DSD	MAT-III	<b>T E A  B R E A K</b>	PEI	ED	<b>L U N C H  B R E A K</b>	EDI LAB (B1)/ DSD LAB (B2)			
<b>Tue</b>	NT	COA		MAT-III	PEI		EDI LAB (B2)/ DSD LAB (B3)			
<b>Wed</b>	ED	NT		DSD	MAT-III		EDI LAB (B3)/ DSD LAB (B1)			
<b>Thu</b>	MAT-III			ED	DSD		NT (T)			
<b>Fri</b>	PEI	ED (T)		COA	NT		CPC			
<b>Sat</b>	COA	DSD (T)		Technical Seminar/Workshops			Association Activities			

**Subject Code**

18MAT31

**Subject**

Transform Calculus, Fourier Series and Numerical Techniques

Maths-III

**Faculty**

Prof. Girish Y.

18EC32

Network Theory

NT

Dr. Subhas Meti

18EC33

Electronic Devices

ED

Prof. Sharan Kumar.

18EC34

Digital System Design

DSD

Prof. Sunil B.

18EC35

Computer Organization & Architecture

COA

Prof. Santosh S. Bujari

18EC36

Power Electronics & Instrumentation

PEI

Prof. Kiran Kotin

18ECL37

Electronics Devices & Instrumentation Laboratory

EDI Lab

Dr. Subhas Meti, Mr. Basavaraj Soratur

18ECL38

Digital System Design Laboratory

DSD Lab

Prof. Sunil B. & Prof. Shashidhar P. K.

18CPC39

CIP, Ethics & Cyber Law

CPC

Adv. B. S. Baleshwarmath

**Coordinator**

**HOD**

**PRINCIPAL**

**Class Room: 5002 (Ground Floor)**

**W E F: 01-09-2020**

**Class Teacher: Prof. Maltesh I. G.**

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30
<b>Mon</b>	PCS	DSP	<b>T E A  B R E A K</b>	TIME	EMW	<b>L U N C H  B R E A K</b>	DSP LAB (B1) / HDL LAB (B2)		
<b>Tue</b>	ITC	EMW		VHDL	DSP		DSP LAB (B2) / HDL LAB (B3)		
<b>Wed</b>	TIME	PCS (T)		EMW (T)	VHDL		DSP LAB (B3) / HDL LAB (B1)		
<b>Thu</b>	EMW	ITC		DSP	PCS		Association Activity		
<b>Fri</b>	VHDL	TIME		PCS	ITC		ES	Association Activity	
<b>Sat</b>	DSP (T)			Technical Seminar/Workshops			Association Activity		

**Subject Code**

**Subject**

18ES51	Technological Innovation Management And Entrepreneurship
18EC52	Digital Signal Processing
18EC53	Principles of Communication Systems
18EC54	Information Theory & coding
18EC55	Electromagnetic Waves
18EC56	Verilog HDL
18ECL57	Digital Signal Processing Laboratory
18ECL58	HDL Laboratory
18CIV59	Environmental Studies

TIME	Prof. Sunil B.
DSP	Prof. Maltesh I. G.
PCS	Prof. S. V. Mahapurush
ITC	Prof. Shashidhar P. K.
EMW	Prof. Basavaraj S. S.
VHDL	Prof. Sanjay V. Chowdhary
DSP Lab	Prof. Maltesh I. G.
HDL Lab	Prof. Manjunathraddi Bentur
ES	Prof.

**Faculty**

**Coordinator**

**HOD**

**PRINCIPAL**

SMT KAMALA & SRI VENKAPPA M. AGADI COLLEGE OF ENGINEERING & TECHNOLOGY, LAXMESHWAR.  
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.  
**7<sup>th</sup> SEMESTER TIME TABLE**

**Class Room: 4208 (2<sup>nd</sup> Floor)**

**W E F: 01-09-2020**

**Class Teacher: Prof. Manjunathraddi Bentur**

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30	
<b>Mon</b>	PE	MMC/RTS	<b>T E A  B R E A K</b>	MWA	DIP	<b>L U N C H  B R E A K</b>	ADC LAB (B1) / VLSI LAB (B2)			
<b>Tue</b>	DIP	PE		SC	MWA		ADC LAB (B2) / VLSI LAB (B3)			
<b>Wed</b>	SC	DIP		PE	MMC/RTS		ADC LAB (B3) / VLSI LAB (B1)			
<b>Thu</b>	MMC/RTS	MWA		DIP	PE		PWP-I + PS			
<b>Fri</b>	MWA	SC		PWP-I + PS			Association Activity			
<b>Sat</b>	PWP-I + PS			Technical Seminar/Workshops			Association Activity			

**Subject Code**

**Subject**

**Faculty**

17EC71	Microwave & Antennas	MWA	Prof. S. V. Mahapurush
17EC72	Digital Image Processing	DIP	Prof. Manjunathraddi Bentur
17EC73	Power Electronics	PE	Prof. Kiran K. Kotin
17EC741	Multi Media Communication	MMC	Prof. Santosh Bujari
17EC743	Real Time Systems	RTS	Dr. Sharan Kumar
17EC755	Satellite Communication	SC	Prof. Shashidhar P. K.
17ECL76	Advanced Communication Lab	ADC LAB	Prof. Sanjay V. Chowdhary
17ECL77	VLSI Lab	VLSI LAB	Prof. Kiran Kotin & Prof. Santosh Bujari
17ECP78	Project work Phase-I + Project work Seminar	PWP-I + PS	Prof. Sanjay V. Chowdhary

**Coordinator**

**HOD**

**Principal**

**MASTER TIME TABLE**  
**Department of Electronics & Communication Engineering, SKSVMACET, Lakshmeshwar.**  
**ODD SEMESTER (2020-21)** **W. E. F.: 01-09-2020**

	Semester	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30	
<b>Mon</b>	<b>III</b>	DSD	MAT-III	<b>T E A  B R E A K</b>	PEI	ED	<b>L U N C H  B R E A K</b>	EDI LAB (B1)/ DSD LAB (B2)			
	<b>V</b>	PCS	DSP		TIME	EMW		DSP LAB (B1) / HDL LAB (B2)			
	<b>VII</b>	PE	MMC/RTS		MWA	DIP		ADC LAB (B1) / VLSI LAB (B2)			
<b>Tue</b>	<b>III</b>	NT	COA		MAT-III	PEI		EDI LAB (B2)/ DSD LAB (B3)			
	<b>V</b>	ITC	EMW		VHDL	DSP		DSP LAB (B2) / HDL LAB (B3)			
	<b>VII</b>	DIP	PE		SC	MWA		ADC LAB (B2) / VLSI LAB (B3)			
<b>Wed</b>	<b>III</b>	ED	NT		DSD	MAT-III		EDI LAB (B3)/ DSD LAB (B1)			
	<b>V</b>	TIME	PCS (T)		EMW (T)	VHDL		DSP LAB (B3) / HDL LAB (B1)			
	<b>VII</b>	SC	DIP		PE	MMC/RTS		ADC LAB (B3) / VLSI LAB (B1)			
<b>Thu</b>	<b>III</b>	MAT-III			ED	DSD		NT (T)			
	<b>V</b>	EMW	ITC		DSP	PCS		Association Activity			
	<b>VII</b>	MMC/RTS	MWA		DIP	PE		PWP-I + PS			
<b>Fri</b>	<b>III</b>	PEI	ED (T)	COA	NT	CPC					
	<b>V</b>	VHDL	TIME	PCS	ITC	ES	Association Activity				
	<b>VII</b>	MWA	SC	PWP-I + PS			Association Activity				
<b>Sat</b>	<b>III</b>	COA	DSD (T)	Technical Seminar/Workshops			Association Activity				
	<b>V</b>	DSP (T)		Technical Seminar/Workshops			Association Activity				
	<b>VII</b>	Technical Seminar/Workshops		Association Activity			Association Activity				

SMT KAMALA & SRI VENKAPPA M. AGADI COLLEGE OF ENGINEERING & TECHNOLOGY, LAXMESHWAR.

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.**

**7<sup>th</sup> SEMESTER TIME TABLE (Parallel Batch)**

**Class Room: 4208 (2<sup>nd</sup> Floor)**

**W E F: 01-09-2020**

**Class Teacher: Prof. Manjunathraddi Bentur**

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30	
<b>Mon</b>	PE	MMC/RTS	<b>T E A  B R E A K</b>	MWA	DIP	<b>L U N C H  B R E A K</b>	ADC LAB (B1) / VLSI LAB (B2)			
<b>Tue</b>	DIP	PE		SC	MWA		ADC LAB (B2) / VLSI LAB (B3)			
<b>Wed</b>	SC	DIP		PE	MMC/RTS		ADC LAB (B3) / VLSI LAB (B1)			
<b>Thu</b>	MMC/RTS	MWA		DIP	PE		PWP-I + PS			
<b>Fri</b>	MWA	SC		PWP-I + PS			Association Activity			
<b>Sat</b>	Technical Seminar/Workshops			Association Activity			Association Activity			

**Subject Code**

**Subject**

**Faculty**

15EC71	Microwave & Antennas	MWA	Prof. S. V. Mahapurush
15EC72	Digital Image Processing	DIP	Prof. Manjunathraddi Bentur
15EC73	Power Electronics	PE	Prof. Kiran K. Kotin
15EC741	Multi Media Communication	MMC	Prof. Santosh Bujari
15EC743	Real Time Systems	RTS	Dr. Sharan Kumar
15EC755	Satellite Communication	SC	Prof. Shashidhar P. K.
15ECL76	Advanced Communication Lab	ADC LAB	Prof. Sanjay V. Chowdhary
15ECL77	VLSI Lab	VLSI LAB	Prof. Kiran Kotin & Prof. Santosh Bujari
15ECP78	Project work Phase-I + Project work Seminar	PWP-I + PS	Prof. Sanjay V. Chowdhary

**Coordinator**

**HOD**

**Principal**

**Class Room: 5002 (Ground Floor)**

**W E F: 01-09-2020**

**Class Teacher: Prof. Maltesh I. G.**

	09.30 TO 10.30	10.30 TO 11.30	11.30 TO 11.45	11.45 TO 12.45	12.45 TO 01.45	01.45 TO 02.30	02.30 TO 03.30	03.30 TO 04.30	04.30 TO 05.30
<b>Mon</b>	OS	DSP	<b>T E A  B R E A K</b>	MED	8051	<b>L U N C H  B R E A K</b>	DSP LAB (B1) / HDL LAB (B2)		
<b>Tue</b>	ITC	EMW		VHDL	DSP		DSP LAB (B2) / HDL LAB (B3)		
<b>Wed</b>	MED	8051		OS	VHDL		DSP LAB (B3) / HDL LAB (B1)		
<b>Thu</b>	8051	ITC		DSP	PCS		Association Activity		
<b>Fri</b>	VHDL	MED		OS	ITC		ES	Association Activity	
<b>Sat</b>	DSP (T)			Technical Seminar/Workshops			Association Activity		

<b>Subject Code</b>	<b>Subject</b>		<b>Faculty</b>
15ES51/17ES51	Management & Entrepreneurship Development	MED	Prof. Sunil B.
15EC52/17EC52	Digital Signal Processing	DSP	Prof. Maltesh I. G.
15EC53/17EC53	Verilog HDL	VHDL	Prof. Sanjay V. Chowdhary.
15EC54/17EC54	Information Theory & coding	ITC	Prof. Shashidhar P. K.
15EC553/17EC553	Operating System	OS	Dr. Sharan Kumar
15EC563/17EC563	8051 Microcontroller	8051	Prof. Sanjay V. Chowdhary
15ECL57/17ECL57	DSP Lab	DSP Lab	Prof. Maltesh I. G.
15ECL58/17ECL58	HDL Lab	HDL Lab	Prof. Manjunath Bentur.
17CIV18	Environmental Studies	ES	Prof. Laxman Patil (Civil Dept.)

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